



MAIN FEATURES

- Universal input voltage range (85 305 V_{AC})
- Input inrush current limiting
- 750 W rated power (900 W peak for <10 s)
- High efficiency up to 94%
- Single 24 or 48 V_{DC} output voltage available
- Active PFC, EN61000-3-2 compliant (Class C, >25% load)
- Low earth / touch leakage current
- Over temperature, OV, OC and SC protections
- +12 V, 0.3 A; +5 V, 0.72 A Stand-by outputs
- Built-in current sharing and OR-ing for parallel operation and N+1 redundancy
- Remote On / Off signal
- Power good and remote sense signals
- All packages fit 1U applications (1.6" profile)
- ITE safety approval to IEC 62368-1
- Designed to be complied with UL 8750
- RoHS 3 compliant (Directive EU 2015/863)
- 5000 m altitude operation
- PMBusTM digital power-management protocol supported













DESCRIPTION

Rated for IT / Industrial and LED lighting, the DDP1200 UC and PC series of AC-DC power supplies offer increased embedded power in two (2) compact 1U compatible packages, high energy efficiency and wide versatility being optimised for free-air cooling environment.

The series provides a steady 750 W of regulated DC power through 180-305 V_{AC} and 600 W through 85-137 V_{AC} input voltage ranges in a single output of 24 or 48 V_{DC} .

The DDP1200 series come in a U-shaped 1.6" high package (UC) and a variant providing protective vented cage on both AC and DC sides (PC), to facilitate system integration.

By converting AC power at a 94% typical efficiency rate, the DDP1200 series generates very little heat allowing for optimal thermal management.

The series offers a 12 V_{DC} , 0.3 A and a 5 V_{DC} , 0.7 A stand-by outputs and the full set of protection features including high breaking capacity fuses on both AC lines, input under voltage lockout (IUV), output over-current (OC), output short-circuit (SC), output over-voltage (OV) and over-temperature (OT).

The DDP1200 UC and PC series supports digital power management over the PMBus™ communications protocol enabling interoperation with and easy integration into a system. In addition, analogue control signals include Power Good (P_OK), Remote On / Off (+/-PS_Inhibit) and Sense terminals (RS+, RS-).

Multiple DDP1200 units may be used in parallel mode for redundancy and / or higher power, made possible with the internal OR-ing and current sharing functions.

Being the series conceived and optimised to be operated at free-air cooling environment, therefore without any fan, it is particularly suitable for those environment sensitive to acoustical noise.

The DDP1200 Free Air series complies with the latest IEC/EN/UL 62368-1 safety standard for Audio Video and Information Technologies and was designed to be complied with UL8750.

The DDP1200 Free Air series meets the EN 55032 EMC limits of Class B for conducted and radiated emissions as well as the IEC/EN61000-3 for flicker and harmonics content and the EN 55024, EN 61000-6-2 for EM immunity.





MARKET SEGMENTS AND APPLICATIONS

- Video Wall Display, Entertainment Lighting
- LED Lighting Engine

- Industrial Control System
- Industrial Laser Applications

MODEL CODING AND OUTPUT RATINGS

Model Code	Output Voltages	Packages and Cooling		
IT/Industrial Grade:	24 VDC: - US24-	U-Chassis	U-Chassis + Protective Cages	
DDP1200	48 VDC: - US48-	Natural Convection Cooling: -UC	Natural Convection Cooling: -PC	

Output	24	V	48	V
Parameter	180-305 V _{AC} 163-300 V _{DC}	85-137 V _{AC} 120-163 V _{DC}	180-305 V _{AC} 163-300 V _{DC}	85-137 V _{AC} 120-163 V _{DC}
V1 Nom Voltage	24 V	/DC	48 \	/ _{DC}
V1		±5 %	5 V _{NOM}	
Adjust Range V1	750 W	600 W	750 W	600 W
Rated Power V1	31.2 A	25 A	15.6 A	12.5 A
Rated Current V1	027.	-	.1%	.2.071
Line Regulation V1			2 %	
Load Line Cross Regulation V1				
Ripple & Noise	1 % Peak-to-peak			
V1 Transient response	±5 %V1 to 25 % load change at 1 A/μs			
V1 Over Current Protection	<46.8	3 A	<23.	4 A
V1 Over Voltage protection		116 % V _{NOM} < V	оит < 145 % V _{NOM}	
V1	16000) μF	8000) μF
Max Out Capacitance 12 V _{SB}	12 V _{DC} (sta	nd-by output voltage is refer	Tred to the same V1 output volt	age return)
Nominal Voltage 12 V _{SB} Rated Current	0.3 A	(maximum +12 V _{SB} and +5 V	ss combined output power is 3.	6 W)
12 V _{SB}		120 mV Pe	eak-to-peak	
Ripple & Noise 12 VsB		±.	5 %	
Line Cross Regulation 5 V _{SB}	5 V _{DC} (stand-by output voltage is referred to the same V1 output voltage return)			
Nominal Voltage 5 V _{SB}	0.72 A (maximum +12 V _{SB} and +5 V _{SB} combined output power is 3.6 W)			
Rated Current 5 V _{SB}	50 mV Peak-to-peak			
Ripple & Noise 5 V _{SB}			5%	
Load, line cross Regulation	±3 70			





INPUT SPECIFICATIONS

Specification	Test Conditions / Notes	Min.	Nominal	Max.	Units
AC Input Voltage	PS starts at 85 V _{AC} at all load conditions				
	Operating input voltage range	85	100-277	305	V_{RMS}
	DDP1200 is designed to operate with a square or				
	trapezoidal input voltage wave form (i.e. from UPS)				
DC Input Voltage	Built in fuses has been safety certified up to 250V _{DC} .				
	Operating the DDP1200 above that limit up to 300	120	_	300	V_{DC}
	V _{DC} , does require an external fuse protection. (*)				- 50
Input Frequency	400 Hz (max 440 Hz) operation over 85 – 137 V _{AC}				
input i requeitoy	input range	47	50/60	63	Hz
Input Current	At 180 V _{AC} , 750 W, 50 / 60 Hz			5.0	
input ourient	At 85 V _{AC} , 600 W load, 50 / 60 Hz			8.7	A_{RMS}
	163 V _{DC} , 750 W	-	-	5.6	
	120 V _{DC} , 600 W			6.0	Α
Inrush Current (peak)	At power-on asserted			0.0	
illiusii cui rent (peak)	Cold start, 25 °C ambient, full load				
				20	
	Any point of the AC input sine 230 V _{AC}	-	-	30	Α
F (*)	277 V _{AC}	-	-	50	
Fusing (*)	High breaking, 16 / 20 A, 277 V _{AC} (250 V _{DC})	-	-	16 / 20	Α
F.C	on each AC lines.				
Efficiency	24, 48V variants:				
	At 120 V _{AC} , 20% rated load	85			
	50% rated load	92	-	-	
	100% rated load	92	-	-	%
	At 230 V _{AC} , 20% rated load	87			
	50% rated load	93	-	-	
	100% rated load	94	-	-	
Input Power Consumption	At power on, no load, 100-277 V _{AC} range UC/PC	-	6.0	_	
	Stand by, no load, nominal 100-277 V _{AC} range	_	3.5	_	W
Power Factor	Any nominal input line voltage, 50/60 Hz,				
I OWEI Tactor	from 50 to 100% maximum load	0.95	-	-	-
THDi	From 50 to 100% rated load, 100-277 V _{AC} , 50/60 Hz.			20	%
Harmonic Current		c A D	-	20	70
	Complies with EN 61000-3-2 at 230 V _{AC} , 50/60 Hz, Clas		Llood		
Fluctuations and Flicker	Complies with EN 61000-3-2 Class C at 230 V _{AC} , 50/60		юай.		
Fth-1	Complies with EN 61000-3-3 at nominal voltages and f	uli ioad.			
Earth Leakage Current	Normal conditions		170		
	115 V _{RMS} , 60 Hz	-	170	-	μΑ
	230 V _{RMS} , 50 Hz	-	300	-	'
	264 V _{RMS} , 60 Hz (worst case)	-	-	450	
Touch Leakage Current	264 V _{RMS} , 60 Hz				_
	Normal Condition (NC)	-	-	100	μΑ
	Single Fault Condition (SFC)	-	-	500	
Patient Leakage Current	264 V _{RMS} , 60 Hz				
	Normal Condition (NC)	-	-	100	μΑ
	Single Fault Condition (SFC)	_		500	

^(*) Suggested fuse SIBA 5012434.16 and fuse holder SIBA 5105805.1





OUTPUT SPECIFICATIONS

Specification	Test Conditions / Notes	Min.	Nom.	Max.	Units
V1 Output Voltages	±0.5 % set point accuracy		24		
	RS+ closed on +V1, RS- closed on V1 RTN,	-	48	-	V
	at 6% load.				
V1 Output Power Rating	UC, PC variants at 180-305 V _{AC}			750	
	UC, PC variants at 85 – 137 V _{AC}			600	W
12V Output Valtage	Peak, <10 s, after P_Ok asserted high		10	900	1/
12V _{SB} Output Voltage 12V _{SB} Output Current		-	12	-	V
12VSB Output Current	UC and PC packages up to 70 °C	-	-	0.3	Α
5V _{SB} Output Voltage		-	5	-	V
5V _{SB} Output Current	UC and PC packages up to 70 °C	-	-	0.72	Α
V1 Voltage Adjustment Range	Manually by push up and down buttons	-	_	±5	%V1
V1 Load-Line-Cross Regulation	V _{AC} : 85 – 305 V _{RMS} ; I1: 0 – 100%	-	_	±2	%V1
5V _{SB} , 12V _{SB}					
Load-Line-Cross regulation	V _{AC} : 85 – 305 V _{RMS} ; I _{SB} : 0 – 100%	-	-	±5	$%V_{SB}$
V1 Line Regulation	V _{AC} : 85 – 305 V _{RMS}	-	-	±0.1	%V1
Transient Response:	25 % load changes at 1 A/µs				
V1, 12V _{SB} , 5V _{SB}	24 V_{DC} at 1000 μF load / I_{OUT} > 2.5 A	_	_	±5	%V1
Voltage Deviation	48 V _{DC} at 560 μF load / I _{OUT} > 1.25 A			10	$%V_{SB}$
144	12 V _{SB} , 5 V _{SB} at 0-2200 μF load				
V1	Rated load, Peak-to-peak, 20 MHz BW.	-	_	1	%V1
Ripple and Noise	(100 nF ceramic, 10 µF tantalum at load)	10		150	mc
V1 Start-up Rise Time Start-up Delay	85 <v<sub>IN<305, any load conditions V1 in regulation after de-asserting PS_Inhibit</v<sub>	10	-	150 1700	ms
Start-up Delay	V1 in regulation after AC is applied	-	-	2200	
	(worst case: 85 V _{AC})	-	_	2200	ms
	5V _{SB} in regulation after AC is applied	_	_	500	1113
	(worst case: 85 V _{AC})			000	
Turn-on Overshoot	(-	-	10	%V1
		-	-	10	$%V_{SB}$
V1 Hold-up Time	At nominal V _{IN} , full load	10	-	-	
	SEMI F47-0706 compliant at ≥208 V _{AC}				
	50 % sag (104 V)	200	-	-	ms
	30 % sag (145 V)	500	-	-	
	20 % sag (166 V)	1000	-	-	_
Minimum Load Conscitones	V1, 12 V _{SB} , 5 V _{SB}	0	-	1/000	Α
Maximum Load Capacitance	V1: 24 V _{DC}	-	-	16000	μF
V1 Current Sharing Accuracy	V1: 48 V _{DC} Parallel operation up to four units.	-	-	8000	-
VI Current Sharing Accuracy	Two units in parallel at I1 rated load.				
	I-Share signals connected together.				
	RS+, RS- signals connected together and to the				
	load.	40	_	60	%I1
	Max load at start up 750 W, operating 1250 W,			30	
	180÷305 V _{AC}				
	Max load at start up 600 W, operating 1000 W,				
	85÷137 V _{AC}				
V1 Remote Sense	RS+ and RS- power path voltage loss compensation	-	-	0.36	V





OUTPUT POWER DE-RATING CURVES

U-Chassis

Natural convection cooling (UC)

Horizontal mounting

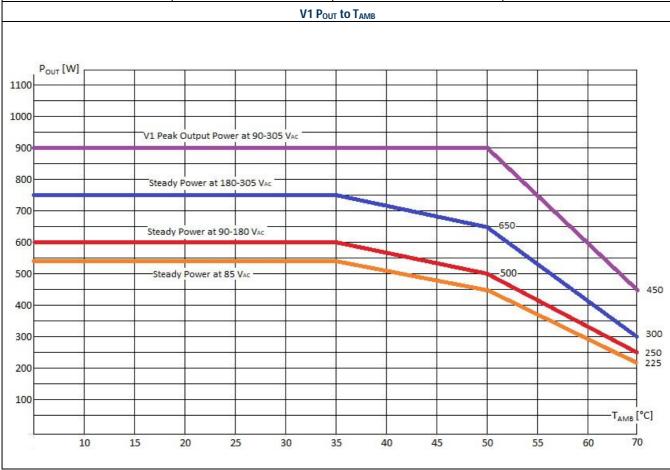


U-Chassis with Protective Cover

Natural convection cooling (PC)

Horizontal mounting









750 W Natural Convection, AC-DC Power Supply

DDP1200 UC/PC FREE AIR SERIES

PMBus

The DDP1200 does support communication according the PMBus 1.2 protocol via SDA, SCL and #SMBALERT signals as defined in the SMBus Specification version 2.0.

The power supply shall not load the SMBus if it has no input power (SCL & SDA lines should go to High-Z).

The pull-up resistors (2.2 k Ω) for these signals shall be external to the power supply and referenced to an external +3.3V bus voltage. The DSP circuits inside the power supply are powered by the standby output.

The PMBus is active whatever input power is applied to the power supply or a parallel redundant power supply in the system, provided that their 12V_{SB} are connected in parallel.

Maximum speed of SMBus is 100 kHz.

The ADDR0 and ADDR1 signals, are inputs to the power supply that control the PMBus address assigned to the power supply. On the system side, the ADDR0 and ADDR1 signals will either be connected to return through a 1 k Ω pull-down resistor or connected to +3.3V external bus voltage through a 1 k Ω pull-up resistor.

The address shall be derived from the logic of this pin as indicated on Outline Drawing and Connections section.

The power supply is a slave only on SMBus device.

For a comprehensive description of DDP1200 PMBus management, do refer to the application note, "AN_MDP-DDP1200 PMBus Mgt_Rev00". Examples of DDP1200 parameters available through communication bus are:

- Input voltage status
- Output voltages +V1 measured value
- Output current on +V1 measured value
- Current sharing status
- Thermal health measured value
- Power-On / Working hours
- Product information
- Status information

Failures shall be reported by PMBus for all failure types:

- Protections failure (OV, OC, OT)
- Voltages out of specification.





BASE SIGNALS / CONTROLS (ACCESSIBLE FROM SIGNAL CONNECTOR P204)

Signal	Notes	Min	Тур.	Max	Unit
+PS_Inhibit (Active High)	Input low voltage (I_{IN} = 0 μ A) Input high voltage (I_{IN} = 500 μ A at 5.5 V) V1 disabled when PS_Inhibit is pulled high V1 enabled when PS_Inhibit is floating or low $5V_{SB}$ and $12V_{SB}$ not affected by PS_Inhibit	0 2.5	-	0.8 5.5	V
-PS_Inhibit (Active Low)	Input low voltage (I_{IN} = -800 μ A at 0 V) Input high voltage (I_{IN} = -200 μ A at 2.5 V) (I_{IN} = 700 μ A at 5.5 V) V1 disabled when -PS_Inhibit is pulled low V1 enabled when -PS_Inhibit is floating or high 5V _{SB} and 12V _{SB} not affected by -PS_Inhibit	0 2.5	-	0.8 5.5	V
Power_OK (*) (PS_OK)	Logic level low (<10 mA sinking) Logic level high (200 µA sourcing) Low to high time after V1 in regulation Power down warning time	- 2.4 150 2	- - -	0.7 3.45 350	V
I_Share	The I_SHARE signals shall be daisy chained among power supplies operating it On a single power supply operating it provides current measurement on V1 of On multiple power supplies operating in parallel, it provides current measurement	output. ement on	master V		
SDA, SCL, #SMBALERT, ADDR0, ADDR1	These are signals which support PMBus communication protocol as specified DDP1200 PMBus Mgt_Rev00.				
RSVD RX, RSVD TX	Mainly intended for internal Efore use, these RX and TX signals - available at may be used to access some DSP functions (monitoring, threshold settings, d These signals work as an UART Rx/Tx port and can also work as a RS-232 Rx/T LINE DRIVERS/RECEIVERS" IC	lebug fun	ctions).		
5V _{SB} Output (**)	Active and in regulation after an 85 <v<sub>AC<305 is applied Not affected by PS_Inhibit. Available on P204, pin#4</v<sub>	-	-	500	ms
12VSB Output (***)	Active and in regulation after an 85 <v<sub>AC<305 is applied Not affected by PS_Inhibit. Available on P204, pin#16</v<sub>	-	-	500	ms

^(*) When V1 is On, a P_OK low may indicates V1 under voltage condition. When two DDP1200 operate in parallel, P_OK low in one unit indicates that it is not sharing the expected amount of current (current sharing fault). A 3.3 kΩ internal pull up to a 3.3 V internal reference voltage is used; do not add any other external pull up.



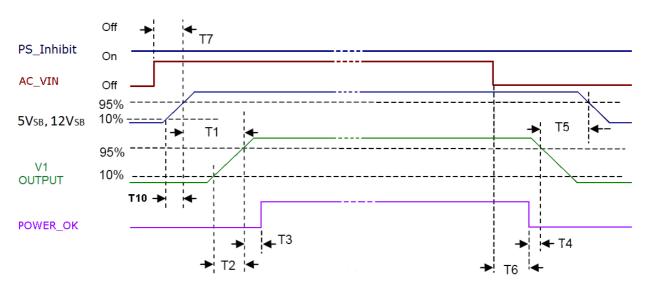
^(**) The 5V_{SB} outputs of two or more DDP1200s operating in parallel, cannot be connected in parallel in turn, since doing so results in power supplies damage.

^(***) The 12V_{SB} outputs of two or more DDP1200s operating in parallel can be connected in parallel in turn, taking into account that the maximum available power will not be higher of a single operating power supply one.



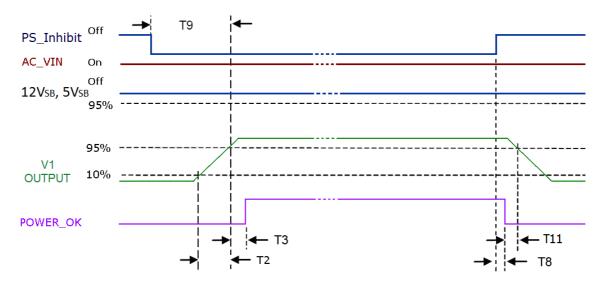
BASE SIGNALS / CONTROLS TIMING

AC/DC input Off-to-On and On-to-Off timings:



12V _{SB} /5V _{SB} On to V1 On	250 ms ≤ T1 ≤ 1700 ms
V1 rise time	10 ms ≤ T2 ≤ 150 ms
12V _{SB} /5V _{SB} rise time	$3 \text{ ms} \le T10 \le 150 \text{ ms}$
V1 On – POWER_OK delay	150 ms ≤ T3 ≤ 350 ms
Power down warning	T4 ≥ 2 ms
V1 Off to 12V _{SB} /5V _{SB} Off	$T5 \ge 0.5 \text{ s (V1 load > 25 W)}$
AC Off to POWER_OK low	<u>T6 ≥ 8 ms</u>
AC_On to 12V _{SB} /5V _{SB} On	T7 ≤ 500 ms

PS_Inhibit Off-to-On and On-to-Off timings:



V1 rise time	10 ms ≤ T2 ≤ 150 ms
V1 On – POWER_OK delay	150 ms ≤ T3 ≤ 350 ms
Turn-Off warning	T11≥1 ms
PS_Inhibit – POWER_OK low delay	T8 ≤ 3 ms
PS Inhibit – V1 On delay	T9 ≤ 1700 ms





PROTECTION FEATURES

Specification	Test Conditions / Notes	Min.	Nominal	Max.	Units
Input Under Voltage	Auto-recovering, hiccup mode.	58	75	82	V _{AC}
Input Fuse	High breaking, 16 / 20 A, 277 V_{AC} (250 V_{DC}) on each AC lines.	-	-	16/20	А
Over Current	At nominal input voltages V1: Hiccup mode, auto-recovering 5 V _{SB} : Auto-recovering 12 V _{SB} : Hiccup mode, auto-recovering	- - -	- - -	150 - -	%I1 _{Rated} A A
Short Circuit	At nominal input voltages V1: Hiccup mode or latch 5 V _{SB} : Auto-recovery 12 V _{SB} : Hiccup mode, auto-recovering.	-	-	-	
Over Voltage	V1, Power shut down, latch off. 12 V _{SB} , Hiccup mode, auto-recovering.	116 -	-	145 150	%V _{NOM}
Over Temperature (ambient)	Hiccup mode, auto-recovering.	70	-	-	°C
Over Temperature (on secondary side)	Hiccup mode, auto-recovering.	-	-	-	°C
Isolation: Primary-to-Secondary	Reinforced	5660 4000	-	-	V _{DC} V _{AC}
Isolation: Input-to-Earth	Basic Production tested at 2642 V _{DC}	2642 1865	-	-	V _{DC} V _{AC}
Isolation: Output-to-Earth	Basic	1500	-	-	V_{AC}
Equipment Protection Class		Class I			

ENVIRONMENTAL SPECIFICATIONS

Specification	Test Conditions / Notes	Min	Nominal	Max	Units
Operating Temperature Range	No de-rating up to 35 °C				
(no De-rating)	See de-rating curves above	-20	-	35	°C
	DDP1200 starts at -40 °C upon warm up delay				
Operating Temperature Range	See de-rating curves and conditions in the Output			70	°C
(with De-rating)	Specifications section	-	-	70	C
Storage Temperature	As per IEC/EN 60721-3-1 Class 1K4	-40		85	°C
Transportation Temperature	As per IEC/EN 60721-3-2 Class 2K4	-40	-	65	C
Humidity	RH, Non-condensing Operating.			90	%
	Non-operating	-	-	95	%
Operating Altitude	Power de-rating above 1800 m	-	-	4000	m
Shock	EN 60068-2-27				
	Operating: Half sine, 30 g, 18 ms, 3 axes, 6x each	(3 positive and	d 3 negative).		
	Non-Operating: Half sine, 50 g, 11 ms, 3 axes, 6x each	(3 positive and	d 3 negative).		
Vibration	EN 60068-2-64				
	Operating: Sine,10 – 500 Hz, 1 g, 3 axes, 1 oct/m	in., 60 min.			
	Random, 5 – 500 Hz, 0.02 g ² /Hz, 1 g _{RN}	ns, 3 axes, 30 m	in.		
	Non-Operating: 5 – 500 Hz, 2.46 g _{RMS} (0.0122 g ² /Hz), 3	3 axes, 30 min.			
MTBF	Full load, 25 °C ambient, 100 % duty cycle,	700.000	-	-	Hours
	Full load, 40 °C ambient, 75 % duty cycle	600.000	-	-	Hours
	Telcordia SR-332 Issue 2				
Useful Life	Nominal V _{IN} , 80 % load, 40 °C ambient (IPC 9592)	-	3	-	Years





ELECTROMAGNETIC COMPATIBILITY (EMC) – EMISSIONS

Phenomenon	Conditions / Notes	Standard	Equipment/Performance Class
Conducted	115, 230, 277 V _{RMS} , Maximum load	EN 55032 EN 55011 (ISM) FCC Part 15	В
Radiated		EN 55032 EN 55011 (ISM) FCC Part 15	B (*)
Line Voltage Fluctuation and Flicker	At 20%, 50% and 100% maximum load Nominal input voltages	EN 61000-3-3	
Harmonic Current Emission	230 V _{AC} input voltage, 50 / 60 Hz 230 V _{AC} , 50 / 60 Hz, >300 W load	EN 61000-3-2 EN 61000-3-2	A, D C

^(°) Performance referred to the enclosed PC package with additional HF chokes on input, output power and signal cables. In any case, radiated emission relevant to both UC and PC package variants, should be assessed at system level.

ELECTROMAGNETIC COMPATIBILITY (EMC) – IMMUNITY

Phenomenon	Conditions / Notes	Standard	Test Level	Criteria
	Reference standard for ITE	EN 55024		
	Reference standard for Industrial/IMS equipment	EN 61000-6-2		
ESD	15 kV air discharge, 8 kV contact, at any point of the system.	EN 61000-4-2	4	А
Radiated Field	10 V/m, 20-2700 MHz, 1 KHz, 80% AM.	EN 61000-4-3	3	Α
Electric Fast Transient	±2 kV on AC power port for 1 minute	EN 61000-4-4	3	Α
Surge	±2 kV line to line; ± 4 kV line to earth on AC power port	EN 61000-4-5	4	Α
Conducted RF Immunity	10 V _{RMS} , 0,15-80 MHz, 1 kHz, 80% AM	EN 61000-4-6	3	Α
Dips and Interruptions	200 – 277 V _{AC} :			
	Drop-out to 0% for 10 ms	EN61000-4-11		A (**)
	Dip to 40% for 5 cycles (100 ms)	EN61000-4-11	A	(de-rate to 500 W)
	Dip to 70% for 25 cycles (500 ms)	EN61000-4-11		Α
	Drop-out to 0% for 5 s	EN61000-4-11		В
	100 – 127 V _{AC} :			
	Drop-out to 0% for 10 ms	EN 61000-4-11		A (**)
	Dip to 40% for 5 cycles (100 ms)	EN 61000-4-11	Д	(de-rate to 240 W)
	Dip to 70% for 25 cycles (500 ms)	EN 61000-4-11	A	(de-rate to 400 W)
	Drop-out to 0% for 5 s	EN 61000-4-11		В

^(**) Performance referred to 5VSB, 12VSB and V1 (PS_OK goes to low level after 8 ms as per timing described at page 8

SAFETY AGENCIES APPROVALS

Certification Body	Safety Standards and file numbers	Category
CSA / UL	CSA C22.2 No.62368-1, UL 62368-1	Audio Video and Information
	C3A C22.2 NO.02300-1, OL 02300-1	Technology Equipment
IEC IECEE	IEC/EN 62368-1	Audio Video and Information
CB Certification	1EG/ EN 02300-1	Technology Equipment
	Directive 2014/35/EU: Electrical Safety: Low Voltage electrical	Audio Video and Information
	equipment (LVD)	Technology Equipment
CE	Directive 2014/30/EU: Electromagnetic Compatibility (EMC)	
	Directive EU 2015/863: RoHS 3	
	Meets all essential requiremets of the standard IEC/EN/UL/CSA 61	010-1 2 nd edition

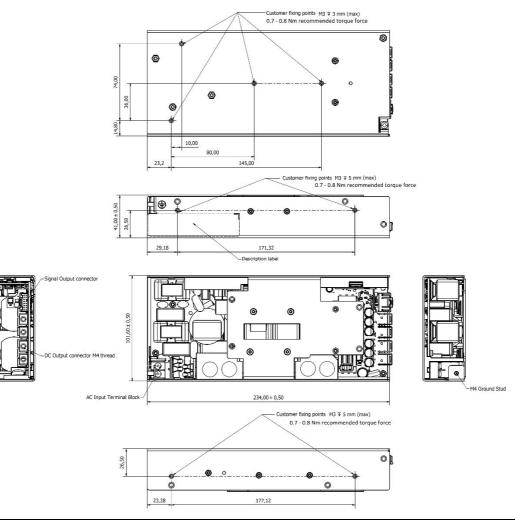


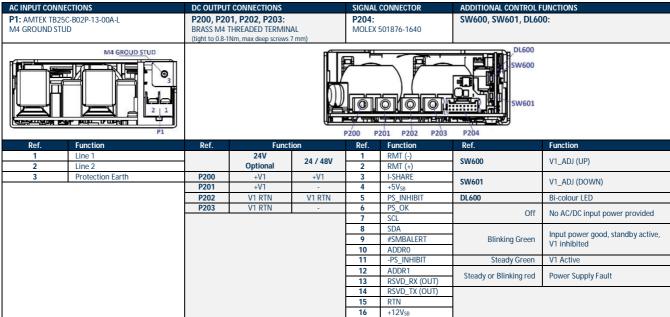


OUTLINE DRAWING AND CONNECTIONS – U-CHASSIS (-UC)

Overall dimensions: 101.6 x 234 x 41.0 mm (4.00 x 9.21 x 1.61 in)

Weight: 1087 g (2.40 lb)





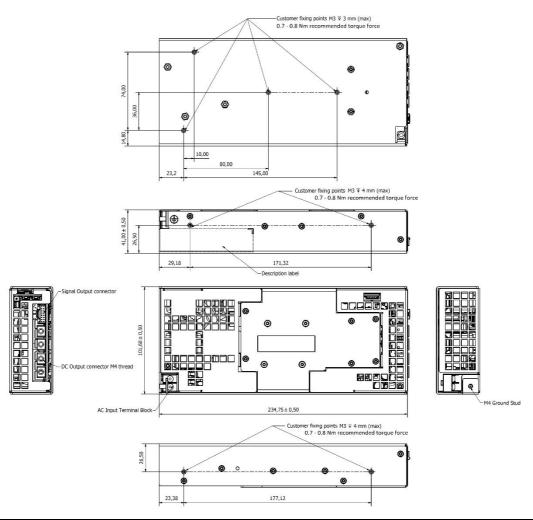


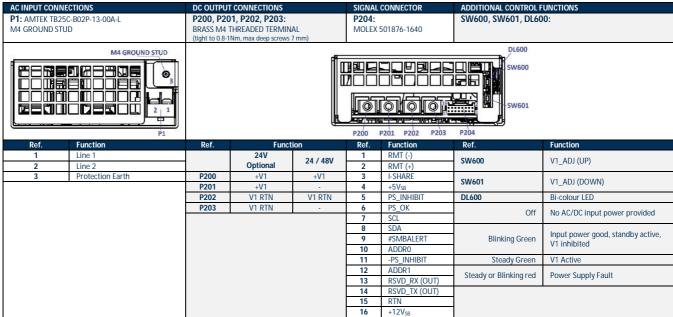


OUTLINE DRAWING AND CONNECTIONS -U-CHASSIS + PERFORATED COVERS (-PC)

Overall dimensions: 101.6 x 234.7 x 41.0 mm (4.0 x 9.21 x 1.61 in)

Weight: 1125 g (2.48 lb)



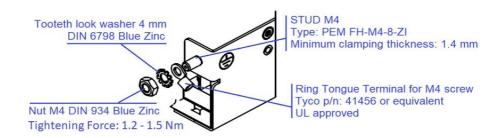








PROTECTION EARTH CONNECTION INSTRUCTIONS



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